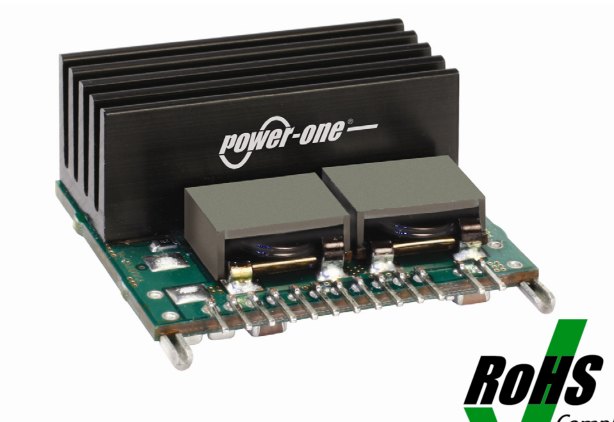
YV09T50 50A DC-DC POL Converter
5V to 13.8V Input • 0.6V to 3.63V Output

Data Sheet

Member of the maXyz Family



RoHS Compliant

Applications

- Low voltage, high density systems with Intermediate Bus Architectures (IBA)
- Point-of-load regulators for high performance DSP, FPGA, ASIC, and microprocessors
- Desktops, servers, and portable computing
- Broadband, networking, optical, and communications systems

Benefits

- One part that covers many applications
- Reduces board space, system cost and complexity, and time to market

Features

- RoHS lead free and lead-solder-exempt products are available
- High efficiency multiphase synchronous buck topology
- Low noise fixed frequency operation
- Wide input voltage range: 5V–13.8V
- High continuous output current: 50A
- Programmable output voltage range: 0.6V–3.63V
- Overcurrent, output overvoltage, and overtemperature protections with automatic restart
- Remote differential output voltage sense
- Power Good signal
- Enable input
- Start up into prebiased load
- No minimum load requirements
- High MTBF of 41.8 MHrs
- Industry standard size through-hole single-in-line package and pinout
  - 1.45"x0.725" (36.83mm x 18.41mm)
- Low height of 1.115" (28.32mm)
- Wide operating temperature range: 0 to 70ºC
- UL94 V-0 flammability rating
- UL60950, CSA C22.2 No. 60950-00, and TUV EN60950-1:2001

Description

The YV09T50-0 non-isolated DC-DC point of load (POL) converter delivers up to 50A of output current in an industry-standard single-in-line (SIP) through-hole package. The YV09T50-0 POL converter is an ideal choice for Intermediate Bus Architectures where point of load conversion is a requirement. Operating from a 5.0-13.8V input the POL converter provides an extremely tightly regulated programmable output voltage of 0.6 V to 3.63V. The POL converters offer exceptional thermal performance, even in high temperature environment with minimal airflow. This performance is accomplished through the use of advanced circuit solutions, packaging and processing techniques. The resulting design possesses ultra-high efficiency, excellent thermal management, and a slim body profile that minimizes impedance to system airflow, thus enhancing cooling for both upstream and downstream devices. The use of automation for assembly, coupled with advanced power electronics and thermal design, results in a product with extremely high reliability.

1. Ordering Information

Y	V	09	T	50	–	0	z
Product Family	**Profile**	**Input Voltage**	**PCB Mounting**	**Output Current**	**Dash**	**ON/OFF Logic**	**RoHS compliance**
POL Converter	Vertical	5V to 13.8V	Through-hole	50A		**Positive Logic**	**No suffix** - RoHS compliant with Pb solder exemption[1] **G** - RoHS compliant for all six substances

[1] The solder exemption refers to all the restricted materials except lead in solder. These materials are Cadmium (Cd), Hexavalent chromium (Cr6+), Mercury (Hg), Polybrominated biphenyls (PBB), Polybrominated diphenylethers (PBDE), and Lead (Pb) used anywhere except in solder.

Example: **YV09T50-0G**: YV09T50-0 POL converter with positive ON/OFF logic and lead-free solder.

2. Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings may cause performance degradation, adversely affect long-term reliability, and cause permanent damage to the converter.

Parameter	Conditions/Description	Min	Max	Units
Input Voltage	Continuous	-0.3	15	VDC
Ambient Temperature Range	Operating	0	70	°C
Storage Temperature (Ts)		-55	125	°C

3. Environmental and Mechanical Specifications

Parameter	Conditions/Description	Min	Nom	Max	Units
Weight				25	grams
MTBF	Calculated Per Telcordia Technologies SR-332 Method I Case 1 50% electrical stress, 40°C ambient		41.8		MHrs
Lead Plating	YV09T50-0 and YV09T50-0G	100% Matte Tin			

4. Electrical Specifications

Specifications apply at the input voltage from 5V to 13.8V, output load from 0 to 50A, output voltage from 0.6V to 3.63V, 470µF external output capacitor, and ambient temperature from 0°C to 70°C unless otherwise noted.

4.1 Input Specifications

Parameter	Conditions/Description	Min	Nom	Max	Units
Input voltage (V_{IN})	$V_{OUT} \leq 0.55 * V_{IN}$	5	12	13.8	VDC
Undervoltage Lockout Turn-On Threshold	Input Voltage Ramping Up	4.25	4.5	4.75	VDC
Undervoltage Lockout Turn-Off Threshold	Input Voltage Ramping Down	3.75	4.0	4.25	VDC
Standby Input Current	V_{IN}=12V, POL is disabled via ON/OFF		16		mADC
Maximum Input Current	V_{IN}=6V, V_{OUT}=3.3V			32	ADC
Input Reflected Ripple Current Peak-to-Peak	BW=5MHz to 20MHz, L_{SOURCE}=1µH, See Figure 19 for setup		60		mA

4.2 Output Specifications

Parameter	Conditions/Description	Min	Nom	Max	Units
Output Voltage Range (V_{OUT})	Programmable with a resistor between TRIM+ and TRIM- pins	0.6		3.63	VDC
Output Voltage Setpoint Accuracy, $V_{OUT} \geq 1V$	V_{IN}=12V, $I_{OUT}=I_{OUT\ MAX}$, 0.1% trim resistor, room temperature	-0.8		0.8	%V_{OUT}
Output Voltage Setpoint Accuracy, $V_{OUT}<1V$	V_{IN}=12V, $I_{OUT}=I_{OUT\ MAX}$, 0.1% trim resistor, room temperature	-8		8	mVDC
Line Regulation, $V_{OUT} \geq 2.5V$	$V_{IN\ MIN}$ to $V_{IN\ MAX}$			0.3	%V_{OUT}
Load Regulation, $V_{OUT} \geq 2.5V$	0 to $I_{OUT\ MAX}$			0.6	%V_{OUT}
Line Regulation, $V_{OUT}<2.5V$	$V_{IN\ MIN}$ to $V_{IN\ MAX}$			9	mVDC
Load Regulation, $V_{OUT}<2.5V$	0 to $I_{OUT\ MAX}$			12	mVDC
Output Voltage Regulation	Over operating input voltage, resistive load, and temperature conditions until the end of life	-1.1		1.1	%V_{OUT}
Output Voltage Peak-to-Peak Ripple and Noise, BW=20MHz, Full Load	V_{IN}=12V, V_{OUT}=0.6V V_{IN}=12V, V_{OUT}=2.5V V_{IN}=12V, V_{OUT}=3.3V		15 20 25	30 40 50	mV mV mV
Dynamic Regulation Peak Deviation Settling Time	V_{IN}=12V, V_{OUT}=3.3V 50 - 100% load step, Slew rate 10A/µs, to 10% of peak deviation		 130 80		 mV µs
Efficiency V_{IN}=12V Full Load Room temperature	V_{OUT}=0.6V V_{OUT}=1.2V V_{OUT}=2.5V V_{OUT}=3.3V		72.8 83.4 90.5 92.3		% % % %
Output Current (I_{OUT})	$V_{IN\ MIN}$ to $V_{IN\ MAX}$	0		50	ADC

Parameter	Conditions/Description	Min	Nom	Max	Units
Turn-On Delay Time[1] POL is Enabled	ON/OFF pin is pulled high From V_{IN}=$V_{IN\ MIN}$ to V_{OUT}=0.1*$V_{OUT.SET}$		0.5	1	ms
Turn-On Delay Time[1] POL is Disabled	V_{IN}=12V From ON/OFF pin changing its state from low to high until V_{OUT}=0.1*$V_{OUT.SET}$		0.5	1	ms
Rise Time[1] C_{OUT}=0 µF, Resistive Load	From V_{OUT}=0.1*$V_{OUT.SET}$ to V_{OUT}=0.9*$V_{OUT.SET}$		1.4	3	ms
Admissible Output Capacitance V_{OUT}≤2.5V	I_{OUT}=$I_{OUT\ MAX}$, Resistive load, ESR>2.5mΩ			5,000	µF
Admissible Output Capacitance V_{OUT}≥2.5V	I_{OUT}=$I_{OUT\ MAX}$, Resistive load, ESR>2.5mΩ			2,200	µF
Switching Frequency	2 phases combined		900		kHz

[1] Total start-up time is the sum of the turn-on delay time and the rise time

4.3 Protection Specifications

Parameter	Conditions/Description	Min	Nom	Max	Units
Output Overcurrent Protection					
Type		Auto-Restart			
Inception Point		55	65	75	A
Output Short Circuit Current (RMS value)	V_{OUT}=2.5V, R_{OUT}<0.01Ω		5.5		A
Autorestart Period	After I_{OUT} drops below inception point		10		ms
Output Overvoltage Protection					
Type		Auto-Restart			
Threshold	I_{OUT}=$I_{OUT\ MAX}$, room temperature	120	125	130	%$V_{O.SET}$
Autorestart Period	After V_{OUT} drops below threshold voltage		0		ms
Overtemperature Protection					
Type		Auto-Restart			
Turn Off Threshold	Temperature is increasing		130		°C
Turn On Threshold	Temperature is decreasing after the POL was shut down by OTP		120		°C
Autorestart Period	After temperature drops below turn-on threshold		3		s
Power Good Signal (PwrGood pin)					
Logic	V_{OUT} is inside the PG window V_{OUT} is outside the PG window	High Low			N/A
Low Output Voltage	I_{SINK}=4mA			0.5	VDC
High Output Voltage	External pull-up	2.4		5.25	VDC

4.4 Feature Specifications

Parameter	Conditions/Description	Min	Nom	Max	Units
Enable (ON/OFF pin)					
ON/OFF Logic	Positive (enables the output when ON/OFF pin is open)				N/A
ON/OFF High Input Voltage	POL is ON	2.4		$V_{IN.MAX}$	VDC
ON/OFF High Input Current	POL is ON			0.5	mADC
ON/OFF Low Input Voltage	POL is OFF	-0.3		1.2	VDC
ON/OFF Low Input Current	POL is OFF			0.12	µADC
Remote Voltage Sense (+VS and –VS pins)					
Voltage Drop Compensation[1]				500	mV

[1] The output voltage measured directly between Vout and GND pins shall never exceed 3.63V

5. Typical Performance Characteristics

5.1 Efficiency Curves

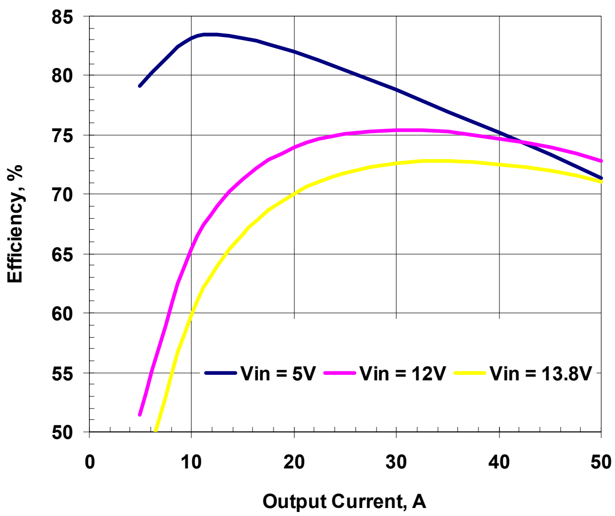


Figure 1. Efficiency vs. Load. Vout=0.6V

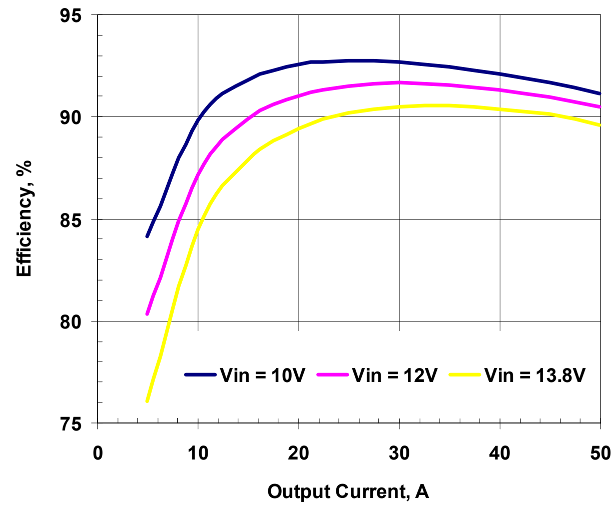


Figure 3. Efficiency vs. Load. Vout=2.5

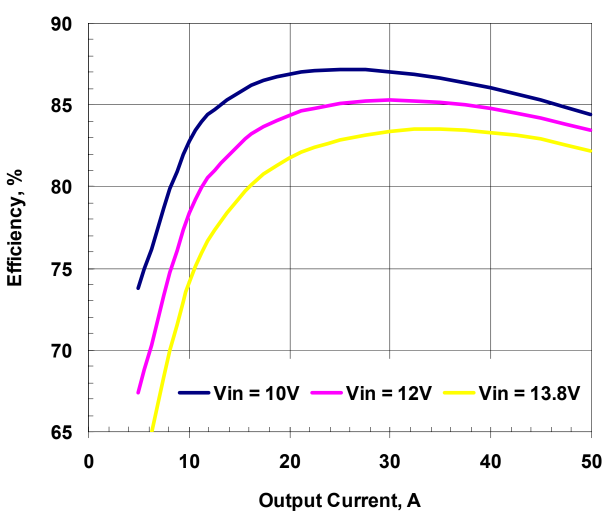


Figure 2. Efficiency vs. Load. Vout=1.2V

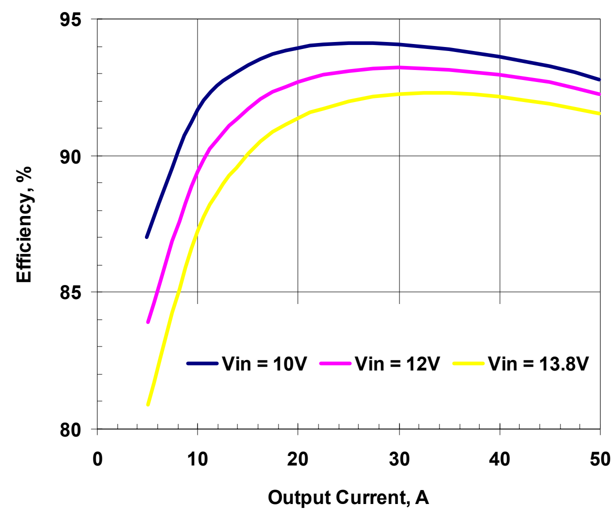


Figure 4. Efficiency vs. Load. Vout=3.3V

5.2 Turn-On Characteristics

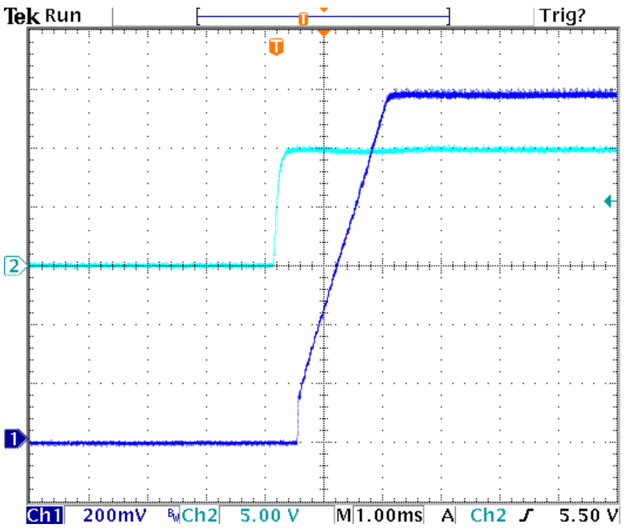


Figure 5. Typical Start-Up Using Remote On/Off (Vo = 1.2 Vdc, Io=50A). Ch1 – Vout, Ch2 – ON/OFF

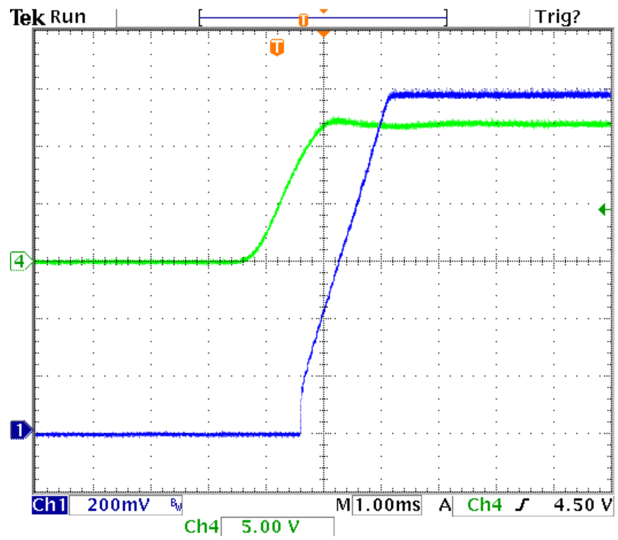


Figure 7. Typical Start-Up with application of Vin (Vo = 1.2Vdc, Io = 50A). Ch1 – Vout, Ch2 – Vin

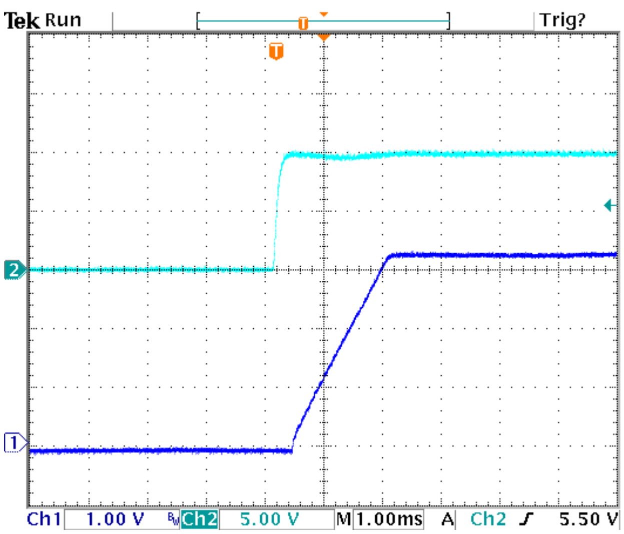


Figure 6. Typical Start-Up Using Remote On/Off (Vo = 3.3 Vdc, Io=50A). Ch1 – Vout, Ch2 – ON/OFF

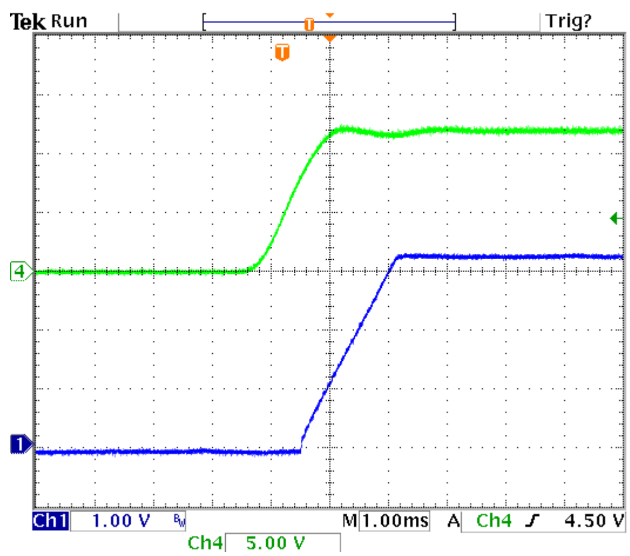


Figure 8. Typical Start-Up with application of Vin (Vo = 3.3Vdc, Io = 50A). Ch1 – Vout, Ch2 – Vin

5.3 Transient Response

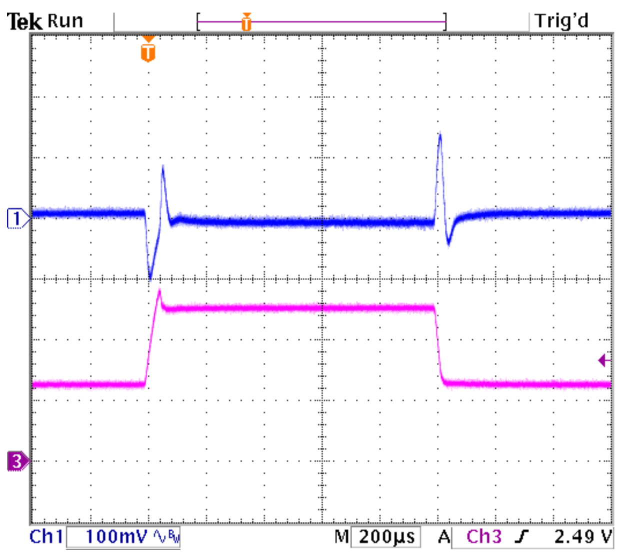


Figure 9. Transient Response to Dynamic Load Change from 50% to 100% of full load (Vin=12V, Vo=0.6Vdc). Ch3 – Iout

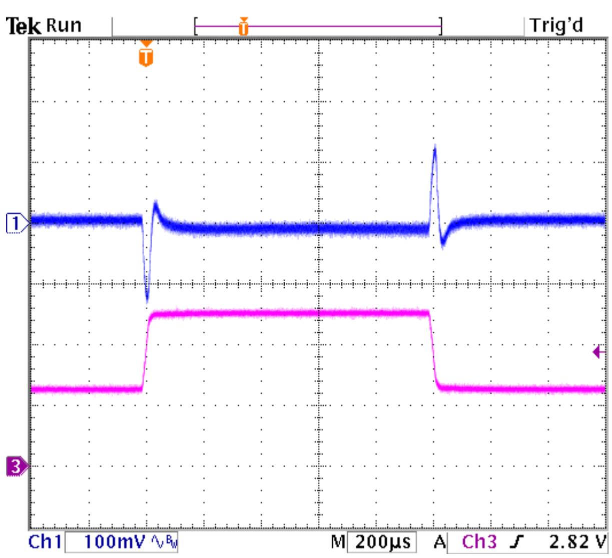


Figure 10. Transient Response to Dynamic Load Change from 50% to 100% of full load (Vin=12V, Vo=3.3Vdc). Ch3 – Iout

5.4 Derating Curves

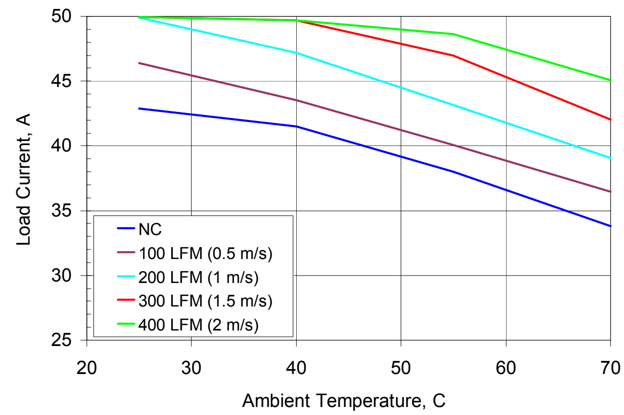


Figure 11 Output Current versus Local Ambient Temperature and Airflow (Vin = 12.0 Vdc, Vo=0.6Vdc)

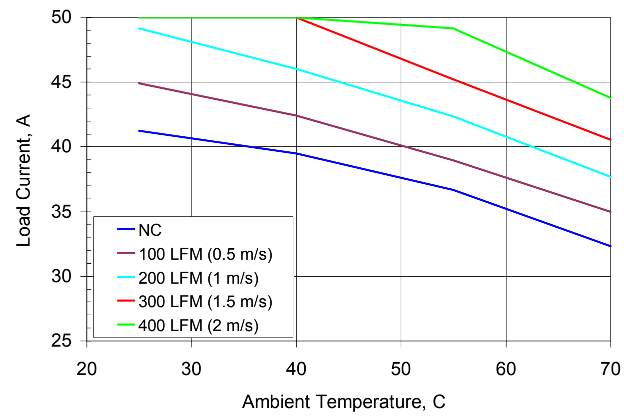


Figure 12. Output Current versus Local Ambient Temperature and Airflow (Vin = 12.0 Vdc, Vo=1.2Vdc).

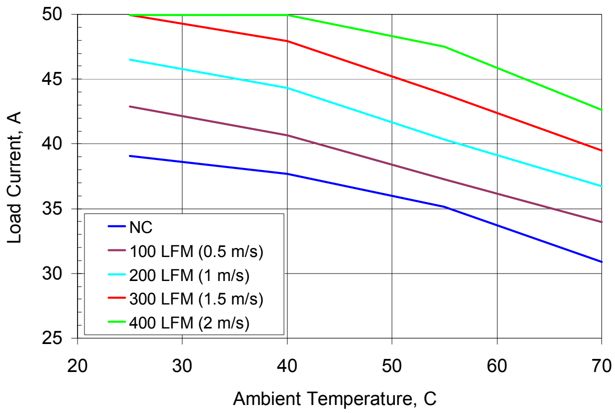


Figure 13 Output Current versus Local Ambient Temperature and Airflow (Vin = 12.0 Vdc, Vo=1.8Vdc).

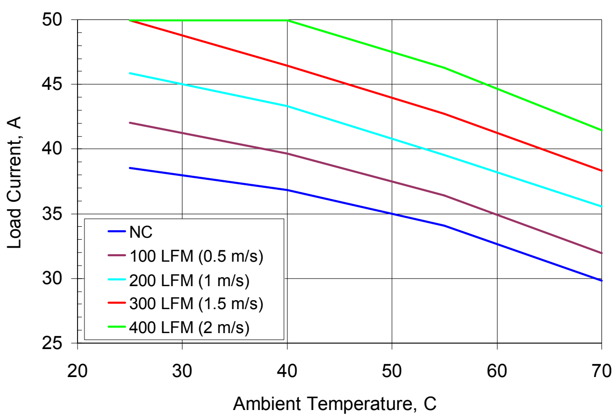


Figure 14. Output Current versus Local Ambient Temperature and Airflow (Vin = 12.0 Vdc, Vo=2.5Vdc).

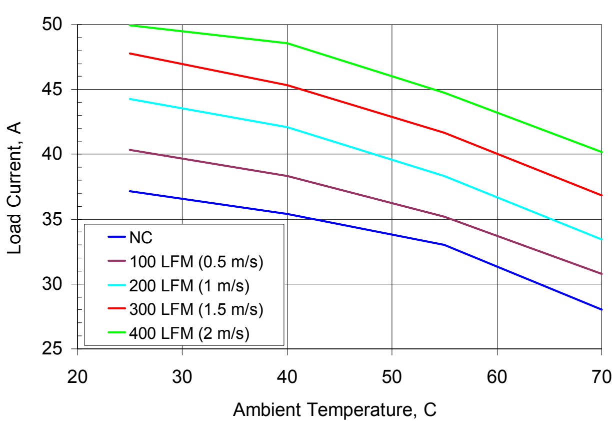


Figure 15. Output Current versus Local Ambient Temperature and Airflow (Vin = 12.0 Vdc, Vo=3.3Vdc).

6. Application Information

6.1 Input and Output Impedance

The POL converter should be connected to the DC power source via low impedance. In many applications, the inductance associated with the distribution from the power source to the input of the converter can affect the stability of the converter. Internally, the converter includes 60µF (low ESR ceramics) of input capacitance which eliminates the need for external input capacitance. However, if the distribution of the input voltage to the POL converter contains high inductance, it is recommended to add a 150µF decoupling capacitor placed as close as possible to the converter input pins. A low-ESR tantalum or POS capacitor connected across the input pins help ensuring stability of the POL converter and reduce input ripple voltage.

A 470µF POS, tantalum, or ceramic output capacitor is recommended to improve output ripple and dynamic response.

It is important to keep low resistance and low inductance of PCB traces for connecting load to the output pins of the converter in order to maintain good load regulation.

6.2 Output Voltage Programming

The output voltage can be programmed from 0.6V to 3.63V by connecting an external resistor R_{TRIM} between Trim+ pin (Pin 8) and Trim- pin (Pin 7), as shown in Figure 16.

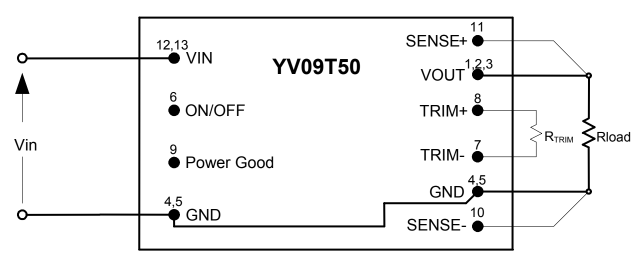


Figure 16. Programming Output Voltage With A Resistor

The trim resistor R_{TRIM} for a desired output voltage can be calculated using the following equation:

$$R_{TRIM} = \frac{1.2}{V_{OUT} - 0.6}, \ \text{k}\Omega$$

where:
R_{TRIM} = Required value of trim resistor in kΩ
V_{OUT} = Desired (trimmed) value of output voltage V

If the R_{TRIM} is not used and the Sense+ and Sense- pins are shorted to VOUT and GND respectively, the output voltage of the POL converter will be 0.6V. No capacitor is allowed between Trim+ and Trim- pins.

Note that the trim resistor tolerance directly affects the output voltage accuracy. It is recommended to use ±0.1% trim resistors to meet the output voltage setpoint accuracy specified in p. 4.2.

Table 1. Trim Resistor Values

V_{OUT}, V	Calculated R_{TRIM}, kΩ	Standard Value of 0.1% Resistor, kΩ
0.6	Open	Open
1.0	3.0	3.01
1.2	2.0	2.0
1.5	1.333	1.33
1.8	1.0	1.0
2.0	0.857	0.856
2.5	0.631	0.634
3.3	0.444	0.442
3.63	0.396	0.397

6.3 ON/OFF (Pin 6)

The ON/OFF pin is used to turn the POL converter ON or OFF remotely by a signal from a system controller. For positive logic, the POL converter is ON when the ON/OFF pin is at a logic high (2.4V min) or left open. The POL converter is OFF when the ON/OFF pin is at a logic low (1.2V max) or connected to GND.

The typical connections are shown in Figure 17.

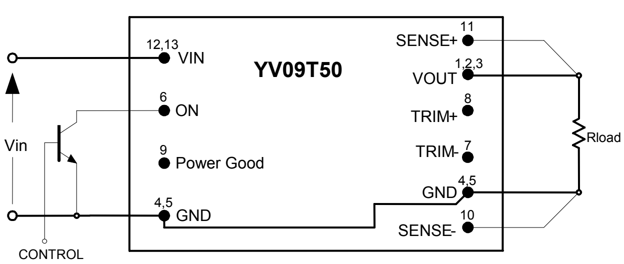


Figure 17. Circuit Configuration For ON/OFF Function

The ON/OFF pin is referenced to ground and typically has 50kΩ input impedance. It has an internal 50kΩ pull-up to 5V supply. It is recommended to control the ON/OFF pin with an open collector transistor or similar device.

6.4 Remote Sense (Pins 10 and 11)

The remote sense feature compensates for the voltage drop between the output pins of the POL converter and the load. The Sense- (Pin 10) and Sense+ (Pin 11) pins should be connected at the load or at the point where regulation is required (refer to Figure 18).

If remote sensing is not required, the Sense pins must be connected to the VOUT and GND pins directly at the output of the POL converter.

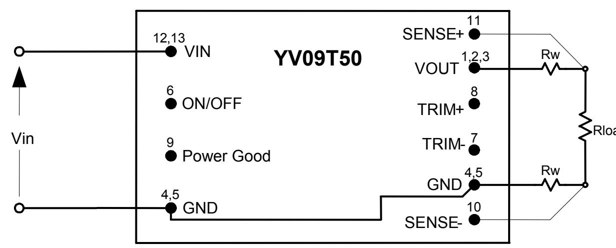


Figure 18. Remote Sense Circuit Configuration

Because the sense leads carry minimal current, large traces on the end-user board are not required. The voltage sense traces should be located close to a ground plane to minimize system noise.

When using remote sense, the output voltage at the converter can be increased by up to 0.5V in order to maintain the required voltage at the load. However, the maximum output voltage measured directly between the VOUT and GND pins shall not exceed 3.63V. In addition it is the user's responsibility to ensure the POL converter's actual output power always remains at or below the maximum allowable output power obtained from the derating curves.

6.5 Protections

6.5.1 Power Good

Power Good pin (Pin 9) is an open drain output, capable of sinking up to 4mA. The Power Good pin is high when the output voltage is within the regulation band. The Power Good pin is at logic low during start-up, undervoltage, overvoltage, or overcurrent conditions, or when the POL converter is disabled via the ON/OFF signal.

6.5.2 Input Undervoltage Lockout

The POL converter will shut down when the input voltage drops below a predetermined voltage. It will start automatically when the input voltage exceeds the specified threshold.

6.5.3 Output Overcurrent Protection

The POL converter is protected against overcurrent and short circuit conditions. Upon sensing an overcurrent condition, the POL converter will shut down. Once the converter has shut down, it will attempt to restart nominally every 10 ms with a typical 2% duty cycle. The attempted restart will continue indefinitely until the overload or short circuit condition is removed.

When the overload or short circuit condition is removed, the POL converter will automatically restart and Vout will return to its nominal value.

6.5.4 Output Overvoltage Protection

The POL converter is protected against overvoltage on the output. If the output voltage is higher than 125% of its nominal value set by the R_{TRIM}, the high side MOSFETs will be immediately turned off and the low side MOSFETs will be turned on. The POL converter will remain in the state until the output voltage reduces below 120% of its nominal value. At that point the POL converter will automatically restart.

6.5.5 Overtemperature Protection

The POL converter will shut down under an overtemperature condition to protect itself from overheating caused by operation outside the thermal derating curves, or operation in abnormal conditions such as system fan failure. After the POL converter has cooled to a safe operating temperature, it will automatically restart.

7. Characterization

7.1 Ripple and Noise

The output voltage ripple and input reflected ripple current waveforms are measured using the test setup shown in Figure 19.

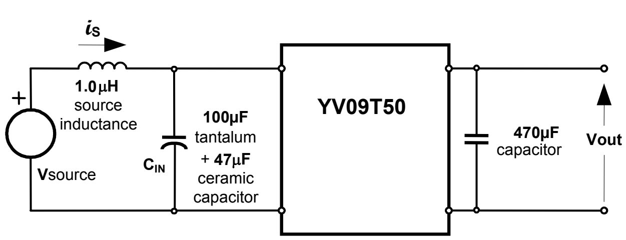


Figure 19. Test Setup For Measuring Input Reflected-Ripple Current And Output Voltage Ripple

8. Safety

The YV09T50-0 POL converters **do not provide** isolation from input to output. The input devices powering YV09T50-0 must provide relevant isolation requirements according to all IEC60950 based standards. Nevertheless, if the system using the converter needs to receive safety agency approval, certain rules must be followed in the design of the system. In particular, all of the creepage and clearance requirements of the end-use safety requirements must be observed. These requirements are included in UL60950 - CSA60950-00 and EN60950, although specific applications may have other or additional requirements.

The YV09T50-0 POL converters have no internal fuse. If required, the external fuse needs to be provided to protect the converter from catastrophic failure. Refer to the “Input Fuse Selection for DC/DC converters” application note on www.power-one.com for proper selection of the input fuse. Both input traces and the chassis ground trace (if applicable) must be capable of conducting a current of 1.5 times the value of the fuse without opening.

To comply with safety agencies’ requirements, a recognized fuse must be used in series with the input line. The fuse must not be placed in the grounded input line. Abnormal and component failure tests were conducted with the POL input protected by three 15A/125VDC fuses connected in parallel. If fuses rated greater than 15A are used, additional testing may be required.

The maximum DC voltage between any two pins is Vin under all operating conditions. In order for the output of the YV09T50-0 POL converter to be considered as SELV (Safety Extra Low Voltage), according to all IEC60950 based standards, the input to the POL needs to be supplied by an isolated secondary source providing a SELV also.

9. Pin Assignments and Description

Pin Name	Pin Number	Pin Type	Buffer Type	Pin Description	Notes
VOUT	1, 2, 3	P		Output Voltage	
GND	4, 5	P		Power Ground	
ON/OFF	6	I	PU	Enable	Pull high or leave floating to turn ON the POL
Trim-	7	I/O	A	Output Voltage Trim	Connect a high accuracy resistor between Trim+ and Trim- pins to set the output voltage
Trim+	8	I	A	Output Voltage Trim	Connect a high accuracy resistor between Trim+ and Trim- pins to set the output voltage
PwrGood	9	I/O	PU	Power Good	Open drain pin indicating status of the output voltage
Sense-	10	I	A	Negative Voltage Sense	Connect to the negative point close to the load
Sense+	11	I	A	Positive Voltage Sense	Connect to the positive point close to the load
VIN	12, 13	P		Input Voltage	
	14, 15			Mechanical Support	Connected to GND inside of the POL converter

Legend: I=input, O=output, I/O=input/output, P=power, A=analog, PU=internal pull-up

10. Mechanical Drawings

All Dimensions are in inches

Tolerances: X.XX: ±0.02” X.XXX: ±0.01”

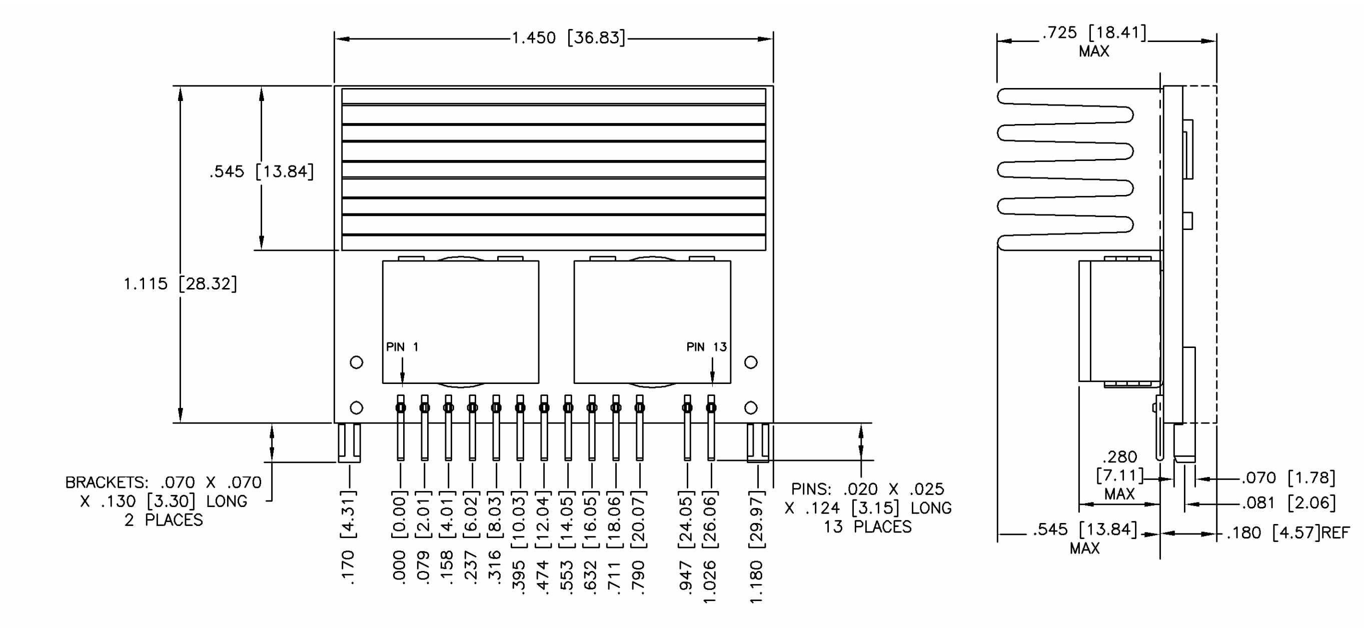


Figure 20. Mechanical Drawing

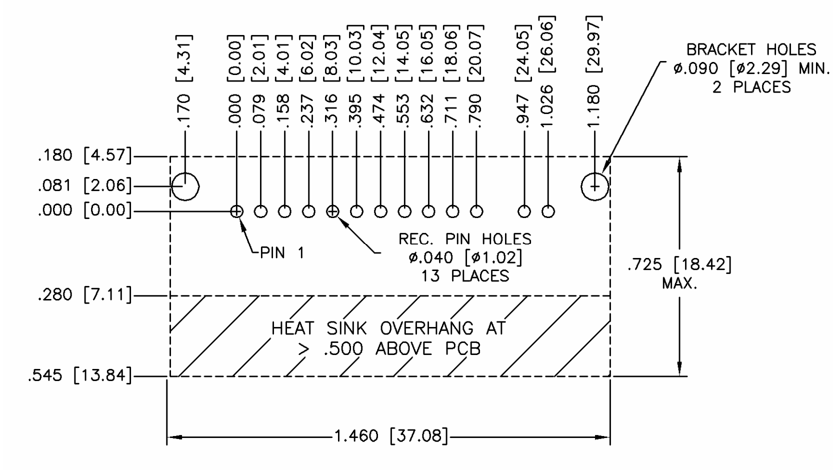


Figure 21. Recommended Footprint – Top View

Notes:

1. NUCLEAR AND MEDICAL APPLICATIONS - Power-One products are not designed, intended for use in, or authorized for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems without the express written consent of the respective divisional president of Power-One, Inc.
2. TECHNICAL REVISIONS - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.